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PATENTS

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** T.C. Coughlin, et al.

**Examiner:** Unassigned

**Serial No:** To be assigned

**Art Unit:** Unassigned

**Filed:** Herewith

**Docket:** END920010050US1 (14637)

**For:** ASIC ARCHITECHTURE FOR ACTIVE-  
COMPENSATION OF A  
PROGRAMMABLE IMPEDANCE I/O

**Dated:** October 18, 2001

Assistant Commissioner for Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

- Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. U.S. Patent No. 5,479,123, issued December 26, 1995;
2. U.S. Patent No. 5,581,197, issued December 3, 1996;
3. U.S. Patent No. 5,602 494, issued February 11, 1997;
4. U.S. Patent No. 5,666,078, issued September 9, 1997;
5. U.S. Patent No. 5,687,330, issued November 11, 1997;
6. U.S. Patent No. 5,705,937, issued January 6, 1998;

**CERTIFICATE OF MAILING BY "EXPRESS MAIL"**

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Assistant Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Dated: 2-7-2002

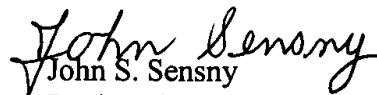
Denise M. Jurik  
Denise M. Jurik

7. U.S. Patent No. 5,726,583, issued March 10, 1998;
8. U.S. Patent No. 5,919,252, issued July 6, 1999; and
9. U.S. Patent No. 6,133,749, issued October 17, 2000.

Applicants are submitting copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

  
John S. Sensny  
Registration No. 28,757

Scully, Scott, Murphy & Presser  
400 Garden City Plaza  
Garden City, New York 11530  
(516) 742-4343

JSS:lf